

IN THE SPECIFICATION

Please replace paragraph 2 on page 3 with the following:

A1
Referring first to Figure 1, a typical prior digital receiver such as used for digital signal processing is illustrated. The receiver includes an analog front end (AFE) 10 having an analog amplifier with automatic gain control (AGC), the gain of which is controllable by a signal on line 18, and an analog-to-digital converter which receives the amplified signal. Samples (S_n) of the input analog signal are obtained within the AFE 10 and coupled to the digital base-band processor (DBP) 11. Generally, for all linear modulations such as quadrature amplitude modulation (QAM), the signal gain level is determined in the DBP and if a gain adjustment is necessary a new gain value is sent to the AFE on line 18.

Please replace paragraph 1 on page 4 with the following:

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To verify that a signal is at the correct gain setting, the amplitude of a signal is measured after the signal is detected in the DBP 11. Before this can occur, however, the signal is processed with for instance, [[in]] the radio frequency interference suppressor (RFI) 12 and signal demodulator (Dmod) 13. Then, the absolute value and truncation of the signal occurs within the ABS 14. Filtering typically now is used, such as with low pass filtering through the filter (FIL) 15. The signal detector (SD) 16 determines when a signal is above a signal detect threshold, thereby verifying that a signal is present. Once a signal has been detected (SIG_FIND) goes high, and then the automatic gain control (AGC) logic 17 compares the signal amplitude to a

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predetermined threshold for a chosen constant number of cycles to determine if the gain is correct. If a gain is not correct, the AGC logic 17 sets a new gain on line 18 in the AFE 10.

Please replace paragraph 2-on page 4 with the following:

A3

The initial start to determine if the AGC level is correct is delayed from the time a signal is received until the signal is processed by the AFE 10 and through the SD 16 of the DBP 11. Then, additional time is required for the AGC computation. Ideally, the AGC calculation should start with zero latency to maximize the useful training period during the preamble, as will be described for the present invention.
